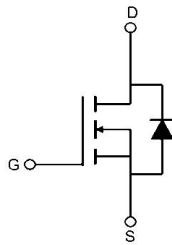


**FH1610DC****N-Channel Trench Power MOSFET****Features**

- 100V, 16A  
 $R_{DS(ON)} = 90m\Omega$  (Max.) @  $V_{GS} = 10V, I_D = 10A$   
 $R_{DS(ON)} = 130m\Omega$  (Max.) @  $V_{GS} = 4.5V, I_D = 3A$
- High Power and Current Handling Capability
- Lead Free Product is Acquired
- Surface Mount Package

**Application**

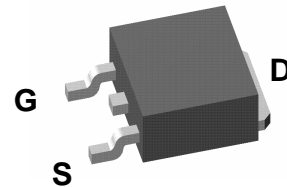
- PWM Application
- Load Switch
- Power Management



Schematic diagram

**TO-252**

Marking and pin assignment



TO-252 top view

**Absolute Maximum Ratings**  $T_C=25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	100	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>note5</sup>	$T_C = 25^\circ\text{C}$	16
$I_D$	Continuous Drain Current <sup>note5</sup>	$T_C = 100^\circ\text{C}$	10
$I_{DM}$	Pulsed Drain Current <sup>note3</sup>		64
$P_D$	Power Dissipation <sup>note2</sup>	$T_C = 25^\circ\text{C}$	35
$I_{AS}$	Avalanche Current <sup>note3,6</sup>		6
$E_{AS}$	Single Pulse Avalanche Energy <sup>note3,6</sup>		9
$R_{\theta JC}$	Thermal Resistance, Junction to Case		3.6
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient <sup>note1,4</sup>		62.5
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150

**Electrical Characteristics**  $T_C=25^\circ\text{C}$  unless otherwise specified

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	100	-	-	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	$\mu A$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.8	3.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10V, I_D = 10A$	-	78	90	$m\Omega$
		$V_{GS} = 4.5V, I_D = 3A$	-	100	130	$m\Omega$
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = 50V, V_{GS} = 0V,$ $f = 1.0MHz$	-	430	-	$\mu F$
$C_{oss}$	Output Capacitance		-	32	-	$\mu F$
$C_{riss}$	Reverse Transfer Capacitance		-	2.3	-	$\mu F$
<b>Switching Characteristics</b>						
$Q_g$	Total Gate Charge	$V_{DS} = 50V, I_D = 10A,$ $V_{GS} = 10V$	-	6	-	nC
$Q_{gs}$	Gate-Source Charge		-	0.98	-	
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.2	-	
$t_{d(on)}$	Turn-On Delay Time	$V_{DS} = 50V, I_D = 10A,$ $R_G = 2\Omega, V_{GS} = 10V$	-	16	-	ns
$t_r$	Turn-On Rise Time		-	3.1	-	
$t_{d(off)}$	Turn-Off Delay Time		-	13	-	
$t_f$	Turn-Off Fall Time		-	2.2	-	
<b>Diode Characteristics</b>						
$I_S$	Continuous Source Current		-	-	16	A
$V_{SD}$	Diode Forward Voltage	$I_S = 10A, V_{GS} = 0V$	-	-	1.0	V
$t_{rr}$	Reverse Recovery Time	$I_{SD} = 10A,$	-	42	-	ns
$Q_{rr}$	Reverse Recovery Charge	$dI_{SD}/dt = 100A/\mu s$	-	61	-	nC

## Notes:

1. The value of  $R_{\theta JC}$  is measured in a still air environment with  $T_A = 25^\circ\text{C}$  and the maximum allowed junction temperature of  $150^\circ\text{C}$ . The value in any given application depends on the user's specific board design.
2. The power dissipation  $P_D$  is based on  $T_{J(MAX)} = 150^\circ\text{C}$ , using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
3. Single pulse width limited by junction temperature  $T_{J(MAX)} = 150^\circ\text{C}$ .
4. The  $R_{\theta JA}$  is the sum of the thermal impedance from junction to case  $R_{\theta JC}$  and case to ambient.
5. The maximum current rating is package limited.
6. The EAS data shows Max. rating. The test condition is  $V_{DS} = 50V, V_{GS} = 10V, L = 0.5mH$

Typical Performance Characteristics

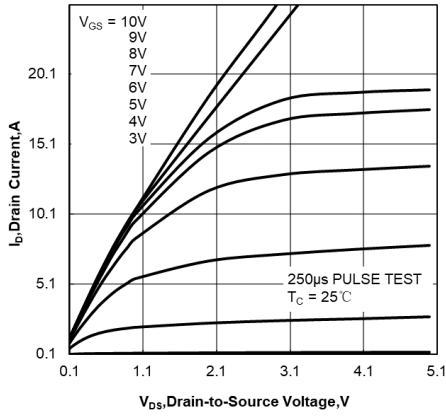


Figure 1. Output Characteristics

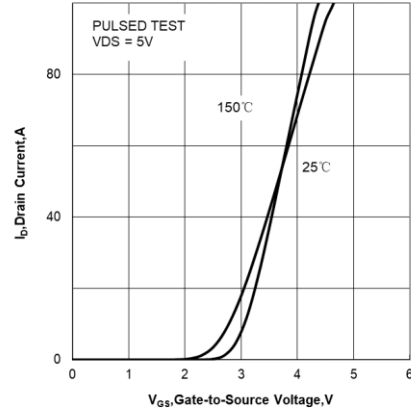


Figure 2. Transfer Characteristics

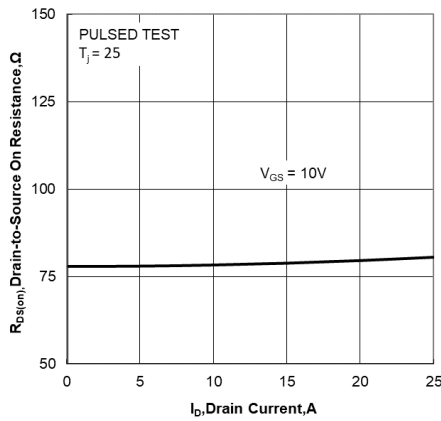


Figure 3. Drain-to-Source On Resistance vs Drain Current

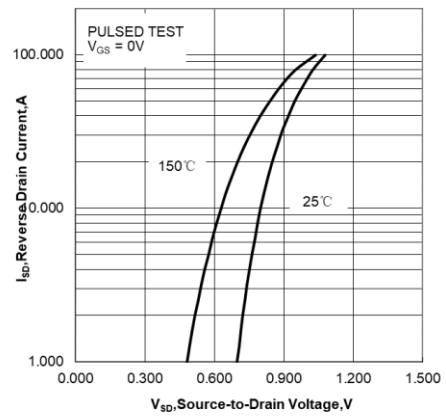


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

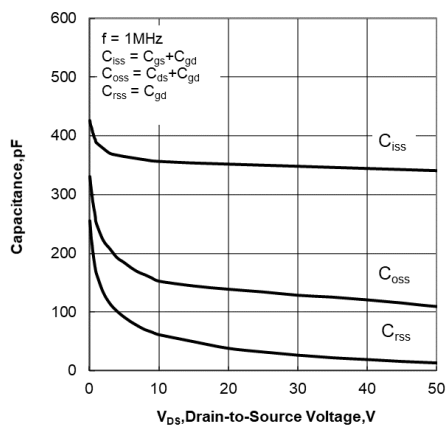


Figure 5. Capacitance Characteristics

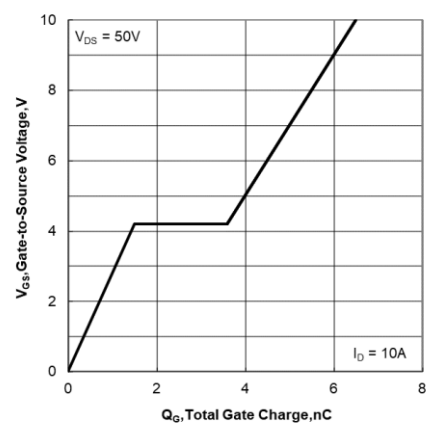


Figure 6. Gate Charge Characteristics

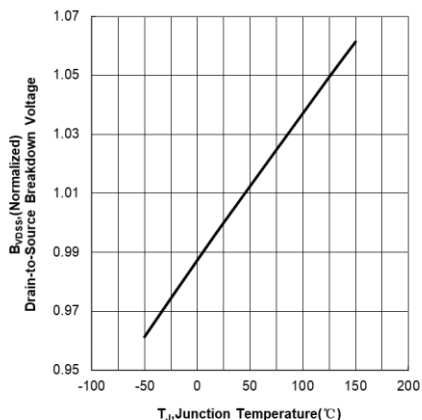


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

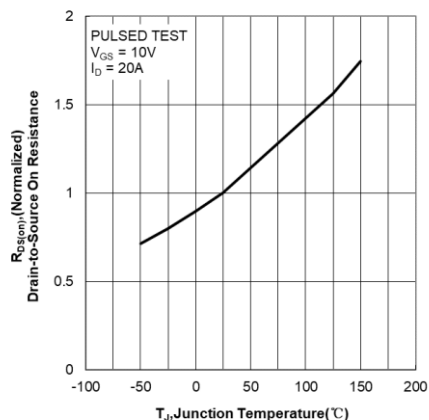


Figure 8. Normalized On Resistance vs Junction Temperature

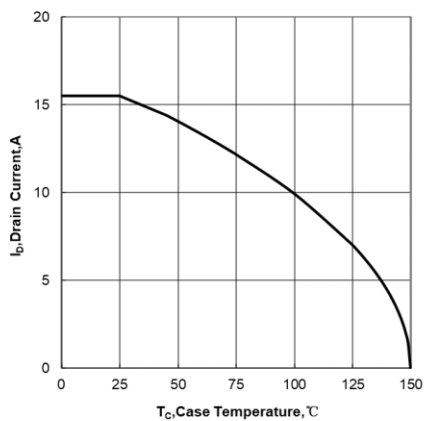


Figure 9. Maximum Continuous Drain Current vs Case Temperature

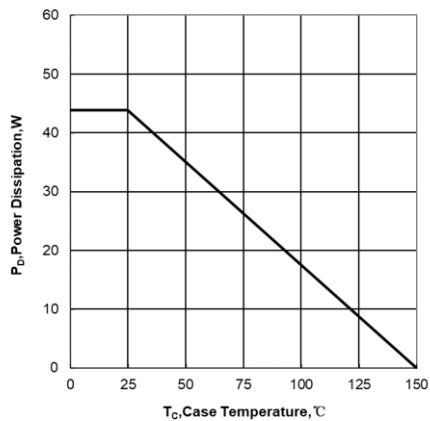
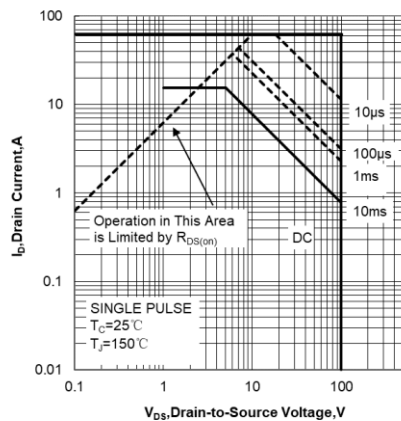
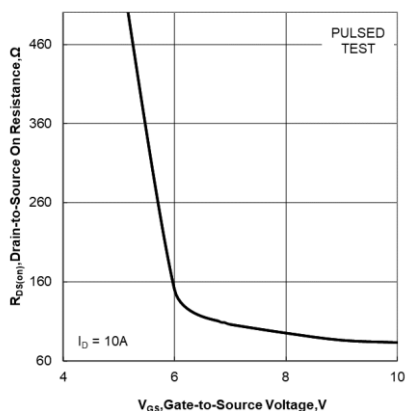
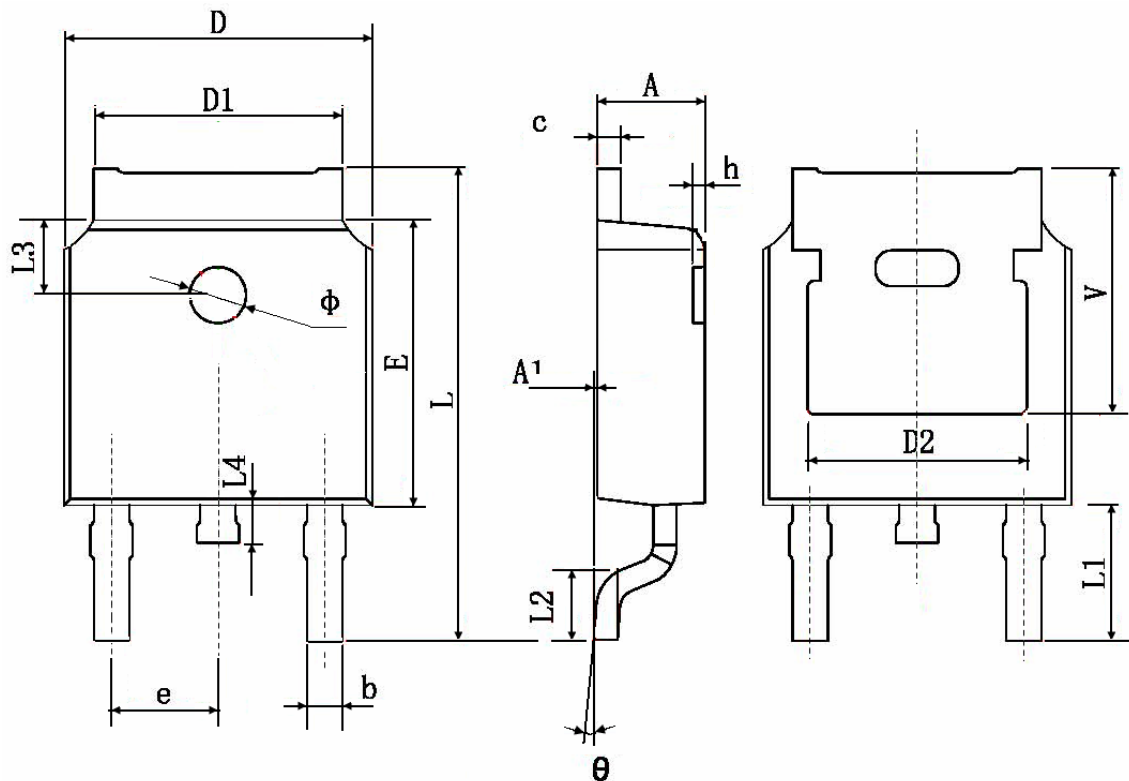


Figure 10. Maximum Power Dissipation vs Case Temperature



## Package Information : TO-252



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.660	0.860	0.026	0.034
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830 TYP.		0.190 TYP.	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.800	10.400	0.386	0.409
L1	2.900 TYP.		0.114 TYP.	
L2	1.400	1.700	0.055	0.067
L3	1.600 TYP.		0.063 TYP.	
L4	0.600	1.000	0.024	0.039
φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.350 TYP.		0.211 TYP.	