

FH15P03B

-30V P-Channel MOSFET

Description

FH15P03B series are from Advanced Power innovated design and silicon process technology to achieve the lowest possible on-resistance and fast switching performance. It provides the designer with an extreme efficient device for use in a wide range of power applications.

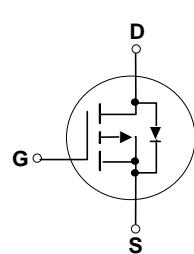
The TO-263 package is widely preferred for all commercial-industrial surface mount applications using infrared reflow technique and suited for high current application due to the low connection resistance. The through-hole version (FH15P03B) are available for low-profile applications.

General Features

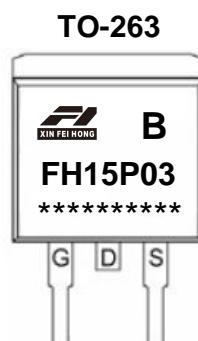
- ◆ $V_{DSS} = -30V$, $I_D = -50A$
- ◆ $R_{DS(ON)} = 11m\Omega$ (MAX) @ $V_{GS} = -10V$
- ◆ Improved dv/dt capability
- ◆ Fast switching
- ◆ 100% EAS Guaranteed
- ◆ Green device available

Applications

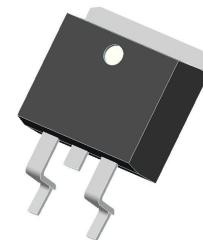
- ◆ Motor Drives
- ◆ UPS
- ◆ DC DC Converter



Schematic diagram



Marking and pin assignment



TO-263 top view

Absolute Maximum Ratings@ $T_f=25^\circ C$ (unless otherwise specified)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D @ T_C=25^\circ C$	Drain Current, V_{GS} @ 10V	-50	A
$I_D @ T_C=100^\circ C$	Drain Current, V_{GS} @ 10V	-28	A
I_{DM}	Pulsed Drain Current ¹	-200	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation	51	W
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C
R_{thj-c}	Maximum Thermal Resistance Junction-case	2.8	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristic						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D = -250\mu\text{A}$	-30	-	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}$, $V_{GS}=0\text{V}$,	-	-	-1	μA
I_{GSS}	Gate to Body Leakage Current	$V_{DS}=0\text{V}$, $V_{GS} = \pm 20\text{V}$	-	-	± 100	nA
On Characteristics						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$R_{DS(\text{on})}$ note3	Static Drain-Source on-Resistance	$V_{GS} = -10\text{V}$, $I_D = -15\text{A}$	-	8	11	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}$, $I_D = -10\text{A}$	-	13	16	
Dynamic Characteristics						
C_{iss}	Input Capacitance	$V_{DS} = -15\text{V}$, $V_{GS}=0\text{V}$, $f=1.0\text{MHz}$	-	2080	-	pF
C_{oss}	Output Capacitance		-	370	-	pF
C_{rss}	Reverse Transfer Capacitance		-	295	-	pF
Q_g	Total Gate Charge	$V_{DS} = -15\text{V}$, $I_D = -20\text{A}$, $V_{GS} = -10\text{V}$	-	30	-	nC
Q_{gs}	Gate-Source Charge		-	4.6	-	nC
Q_{gd}	Gate-Drain("Miller") Charge		-	10	-	nC
Switching Characteristics						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -15\text{V}$, $I_D = -30\text{A}$, $V_{GS} = -10\text{V}$, $R_{\text{GEN}} = 2.5\Omega$	-	11	-	ns
t_r	Turn-on Rise Time		-	9.4	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	24	-	ns
t_f	Turn-off Fall Time		-	12	-	ns
Drain-Source Diode Characteristics and Maximum Ratings						
I_s	Maximum Continuous Drain to Source Diode Forward Current	-	-	-50	A	
I_{SM}	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-200	A	
V_{SD}	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s = -10\text{A}$	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. E_{AS} condition: $T_J=25^\circ\text{C}$, $V_{DD} = -15\text{V}$, $V_G = -10\text{V}$, $R_G = 25\Omega$, $L = 0.5\text{mH}$, $I_{AS} = -22\text{A}$

3. Pulse Test: Pulse Width≤300μs, Duty Cycle≤2%

Typical Performance Characteristics

Figure 1: Output Characteristics

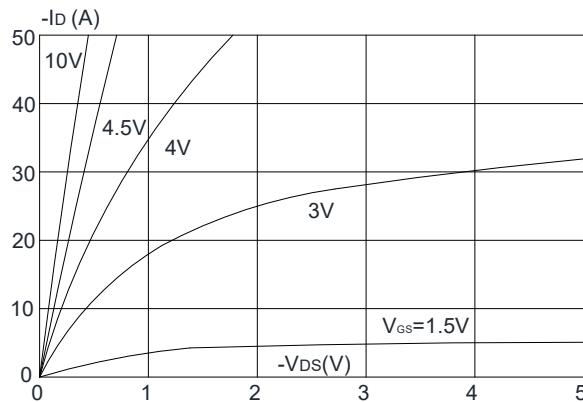


Figure 3: On-resistance vs. Drain Current

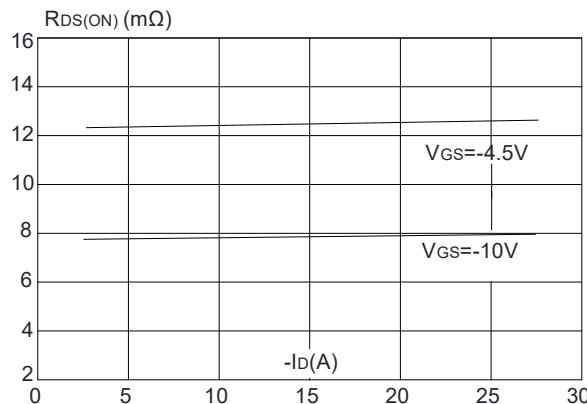


Figure 5: Gate Charge Characteristics

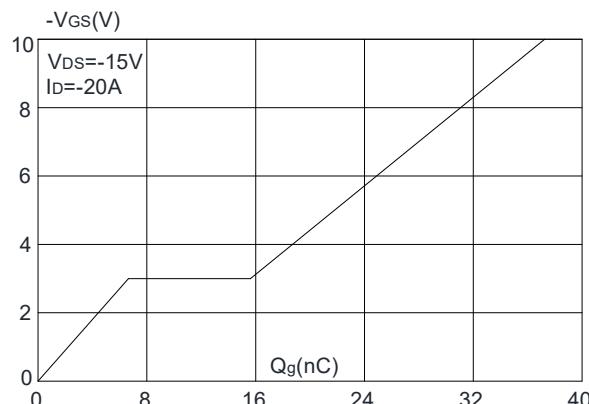


Figure 2: Typical Transfer Characteristics

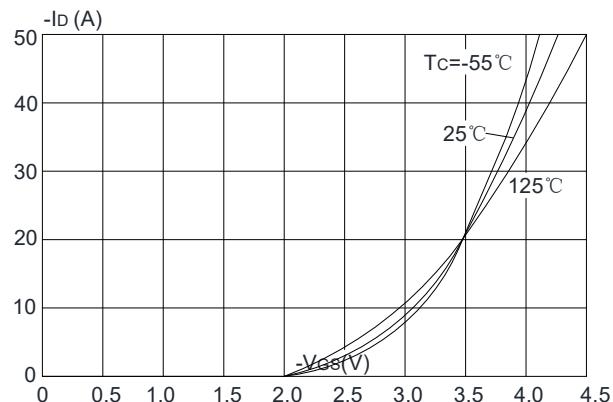


Figure 4: Body Diode Characteristics

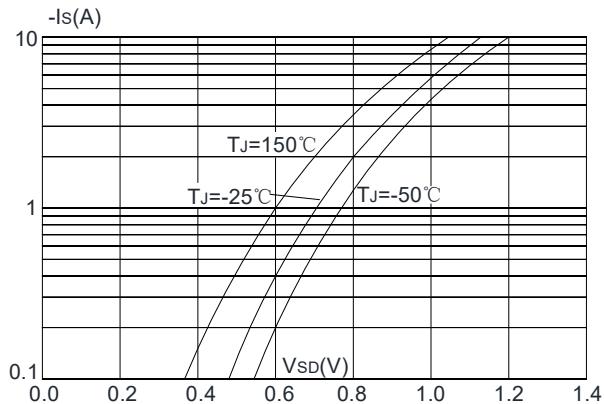


Figure 6: Capacitance Characteristics

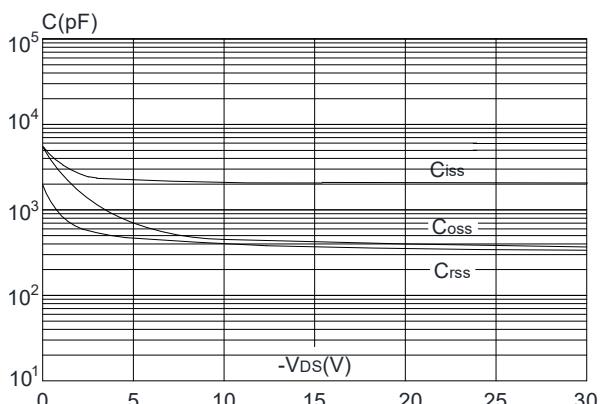


Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

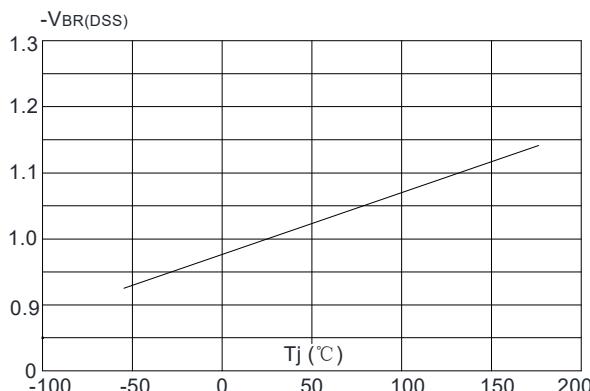


Figure 8: Normalized on Resistance vs. Junction Temperature

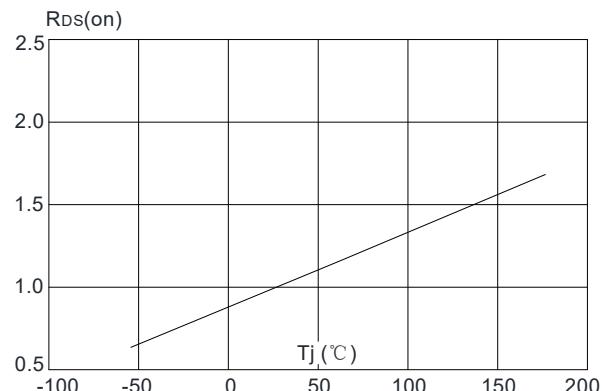


Figure 9: Maximum Safe Operating Area

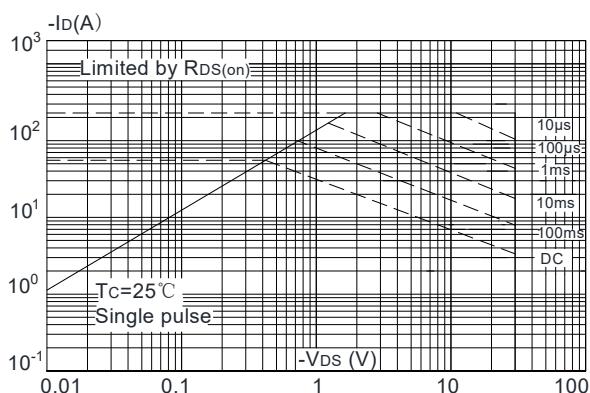


Figure 10: Maximum Continuous Drain Current vs. Case Temperature

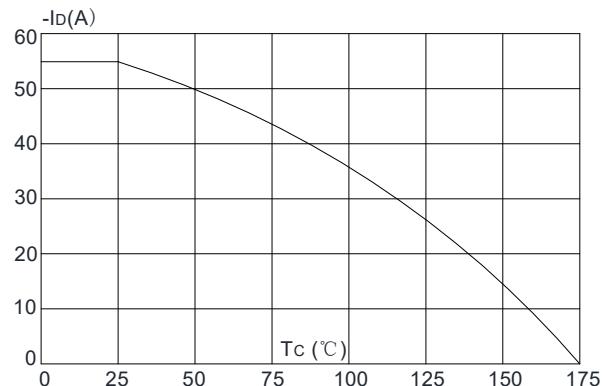
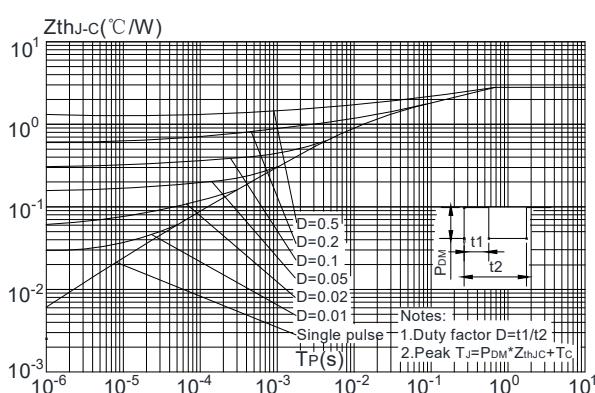
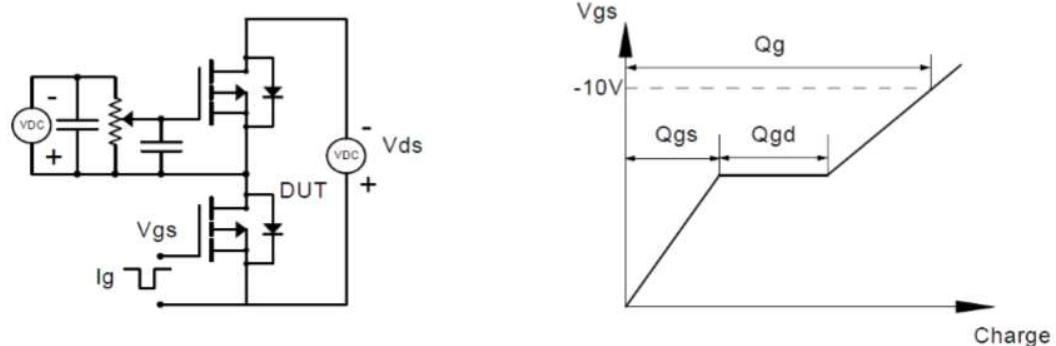


Figure 11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

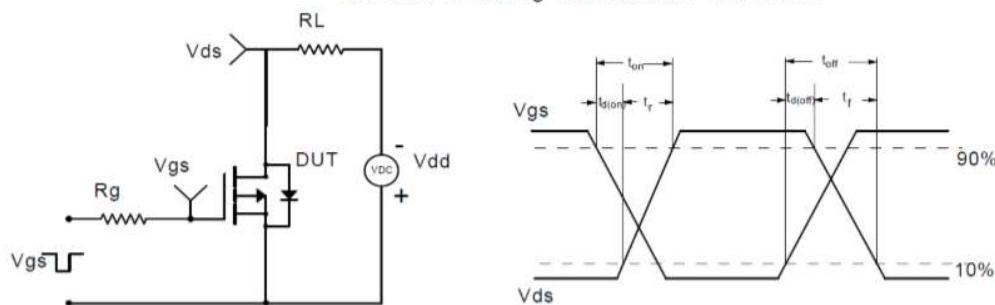


Test Circuit

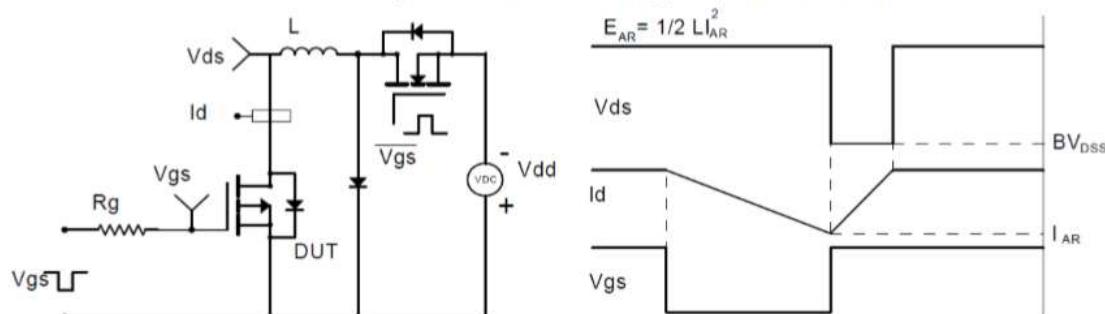
Gate Charge Test Circuit & Waveform



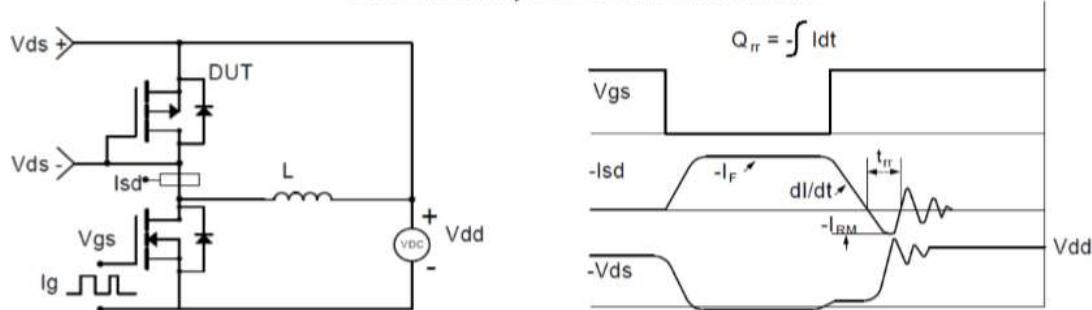
Resistive Switching Test Circuit & Waveforms



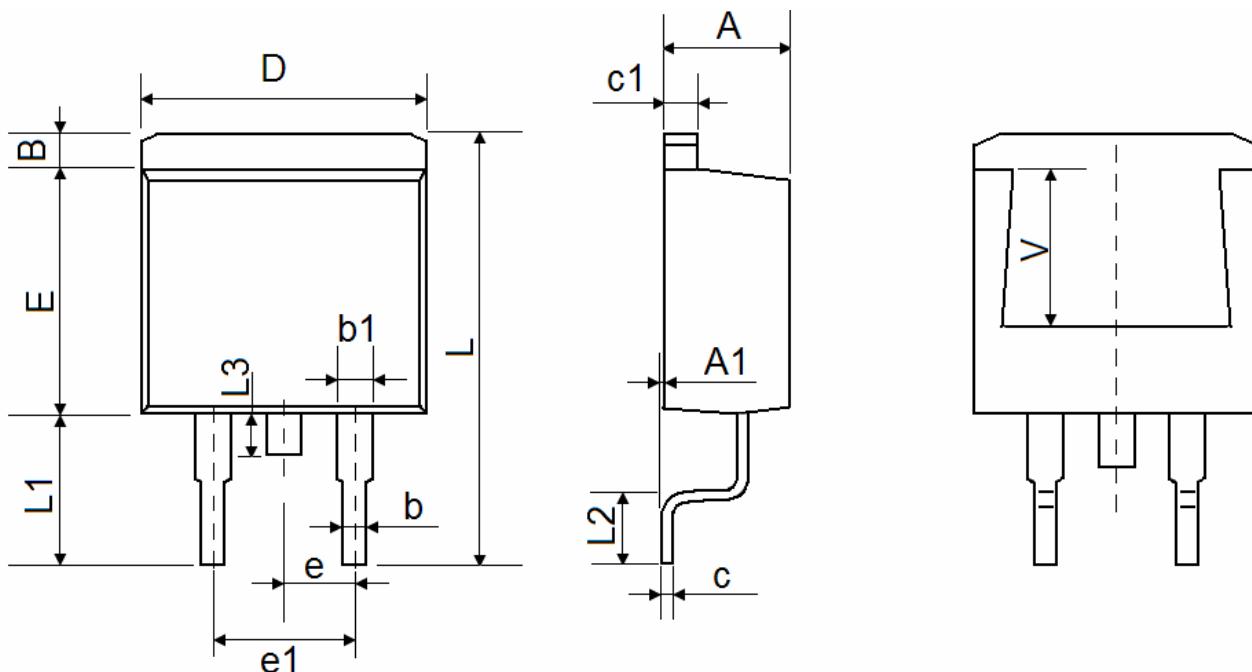
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Information : TO-263



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.470	4.670	0.176	0.184
A1	0.000	0.150	0.000	0.006
B	1.170	1.370	0.046	0.054
b	0.710	0.910	0.028	0.036
b1	1.170	1.370	0.046	0.054
c	0.310	0.530	0.012	0.021
c1	1.170	1.370	0.046	0.054
D	10.010	10.310	0.394	0.406
E	8.500	8.900	0.335	0.350
e	2.540 TYP.		0.100 TYP.	
e1	4.980	5.180	0.196	0.204
L	15.050	15.450	0.593	0.608
L1	5.080	5.480	0.200	0.216
L2	2.340	2.740	0.092	0.108
L3	1.300	1.700	0.051	0.067
V	5.600 REF		0.220 REF	