

FH3401B
P-Channel Enhancement Mode Power MOSFET
Description

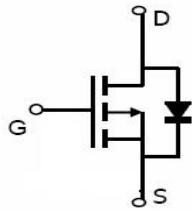
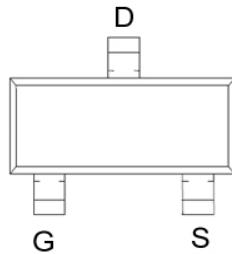
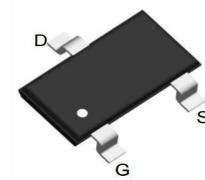
The FH3401B uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch or in PWM applications.

Application

- PWM applications
- Load switch
- Power management

General Features

- $V_{DS} = -30V, I_D = -4.2A$
- $R_{DS(ON)} < 45m\Omega @ V_{GS} = -10V$
- $R_{DS(ON)} < 53m\Omega @ V_{GS} = -4.5V$
- $R_{DS(ON)} < 72m\Omega @ V_{GS} = -2.5V$
- High power and current handing capability
- Lead free product is acquired
- Surface mount package


Schematic diagram

Marking and Pin Assignment

SOT-23 top view
Absolute Maximum Ratings (TA=25°C unless otherwise noted)

| Parameter | Symbol | Limit | Unit |
|--|----------------|------------|------|
| Drain-Source Voltage | V_{DS} | -30 | V |
| Gate-Source Voltage | V_{GS} | ± 12 | V |
| Drain Current-Continuous | I_D | -4.2 | A |
| Drain Current-Pulsed (Note 1) | I_{DM} | -30 | A |
| Maximum Power Dissipation | P_D | 1.25 | W |
| Operating Junction and Storage Temperature Range | T_J, T_{STG} | -55 To 150 | °C |

Thermal Characteristic

| | | | |
|--|-----------------|-----|------|
| Thermal Resistance, Junction-to-Ambient (Note 2) | $R_{\theta JA}$ | 100 | °C/W |
|--|-----------------|-----|------|

Electrical Characteristics (TA=25°C unless otherwise noted)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---|--------------|--|------|-------|-----------|-----------|
| Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=-250\mu A$ | -30 | -34 | - | V |
| Zero Gate Voltage Drain Current | I_{DSS} | $V_{DS}=-24V, V_{GS}=0V$ | - | - | -1 | μA |
| Gate-Body Leakage Current | I_{GSS} | $V_{GS}=\pm 12V, V_{DS}=0V$ | - | - | ± 100 | nA |
| On Characteristics (Note 3) | | | | | | |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{DS}=V_{GS}, I_D=-250\mu A$ | -0.6 | -0.85 | -1.3 | V |
| Drain-Source On-State Resistance | $R_{DS(on)}$ | $V_{GS}=-10V, I_D=-4.2A$ | - | 45 | 54 | $m\Omega$ |
| | | $V_{GS}=-4.5V, I_D=-4A$ | - | 53 | 64 | $m\Omega$ |
| | | $V_{GS}=-2.5V, I_D=-1A$ | - | 72 | 88 | $m\Omega$ |
| Forward Transconductance | g_{FS} | $V_{DS}=-5V, I_D=-4.2A$ | - | 10 | - | S |
| Dynamic Characteristics (Note 4) | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS}=-15V, V_{GS}=0V, F=1.0MHz$ | - | 950 | - | PF |
| Output Capacitance | C_{oss} | | - | 115 | - | PF |
| Reverse Transfer Capacitance | C_{rss} | | - | 75 | - | PF |
| Switching Characteristics (Note 4) | | | | | | |
| Turn-on Delay Time | $t_{d(on)}$ | $V_{DD}=-15V, I_D=-3.2A$ $V_{GS}=-10V, R_{GEN}=6\Omega$ | - | 7 | - | nS |
| Turn-on Rise Time | t_r | | - | 3 | - | nS |
| Turn-Off Delay Time | $t_{d(off)}$ | | - | 30 | - | nS |
| Turn-Off Fall Time | t_f | | - | 12 | - | nS |
| Total Gate Charge | Q_g | $V_{DS}=-15V, I_D=-4A, V_{GS}=-4.5V$ | - | 9.5 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 2 | - | nC |
| Gate-Drain Charge | Q_{gd} | | - | 3 | - | nC |
| Drain-Source Diode Characteristics | | | | | | |
| Diode Forward Voltage (Note 3) | V_{SD} | $V_{GS}=0V, I_S=-1A$ | - | - | -1.2 | V |

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300\mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production

Typical Electrical and Thermal Characteristics

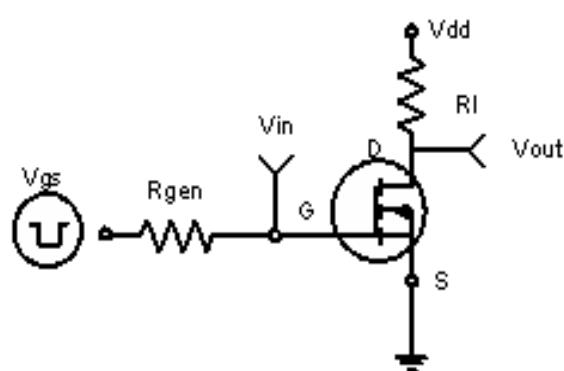


Figure 1: Switching Test Circuit

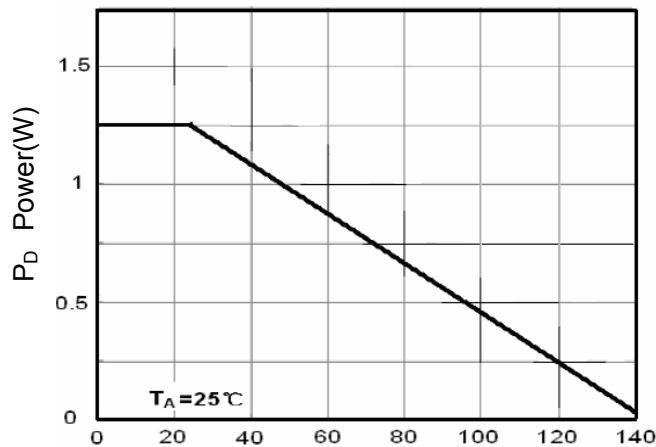


Figure 3 Power Dissipation

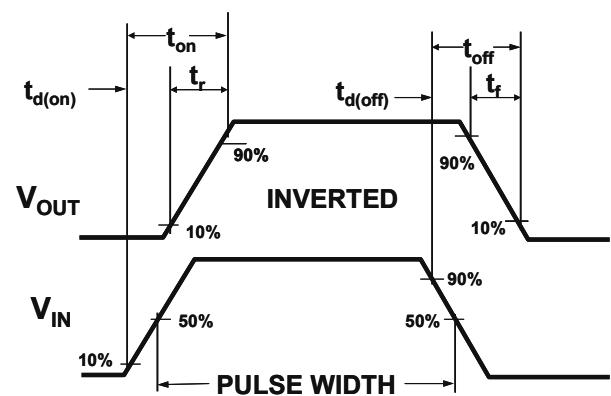


Figure 2: Switching Waveforms

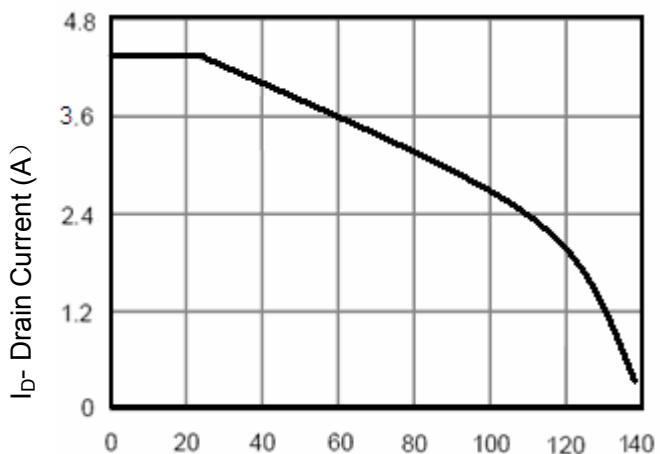


Figure 4 Drain Current

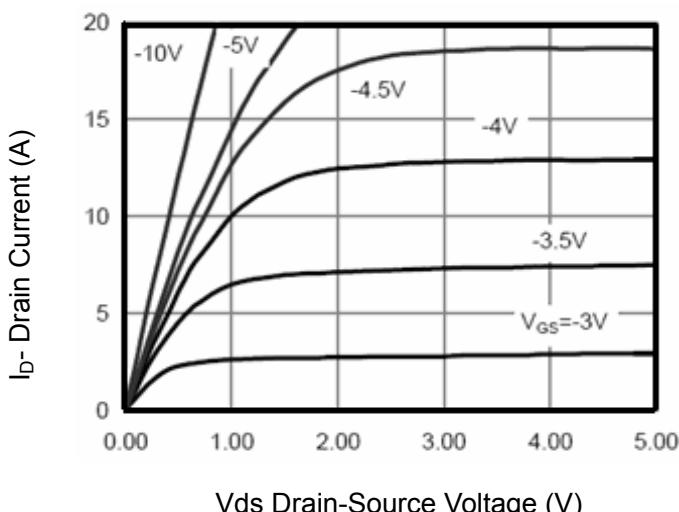


Figure 5 Output Characteristics

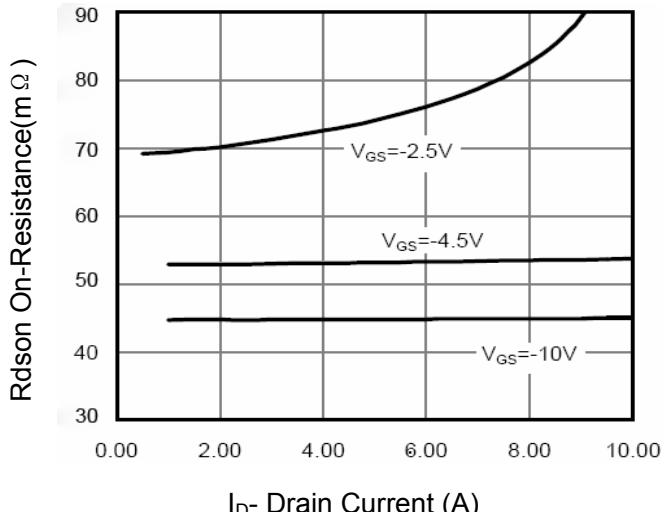


Figure 6 Drain-Source On-Resistance

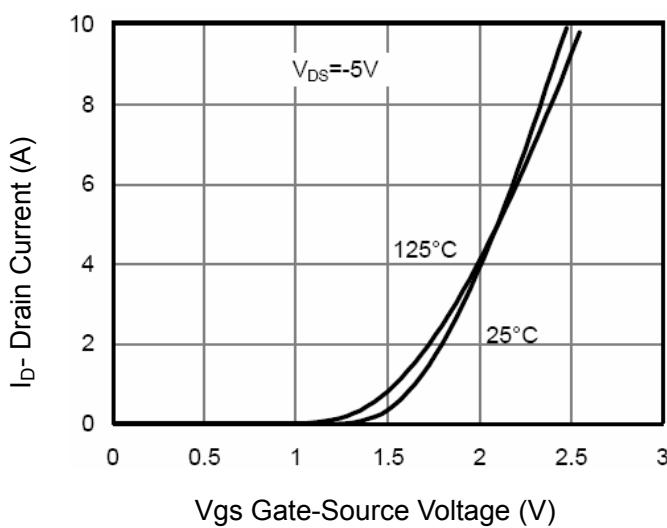


Figure 7 Transfer Characteristics

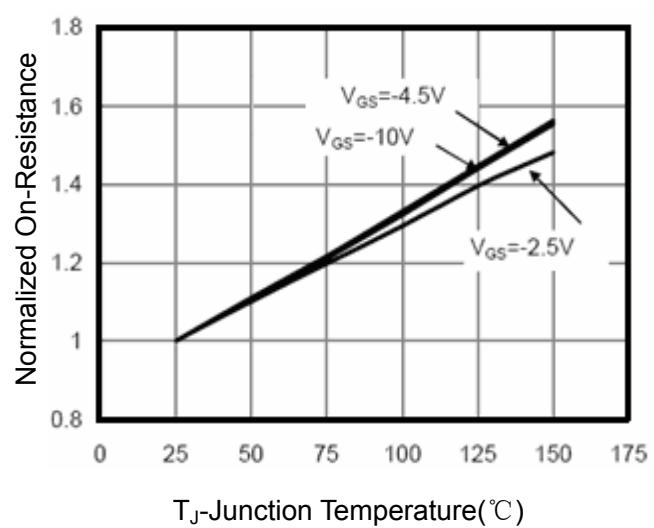


Figure 8 Drain-Source On-Resistance

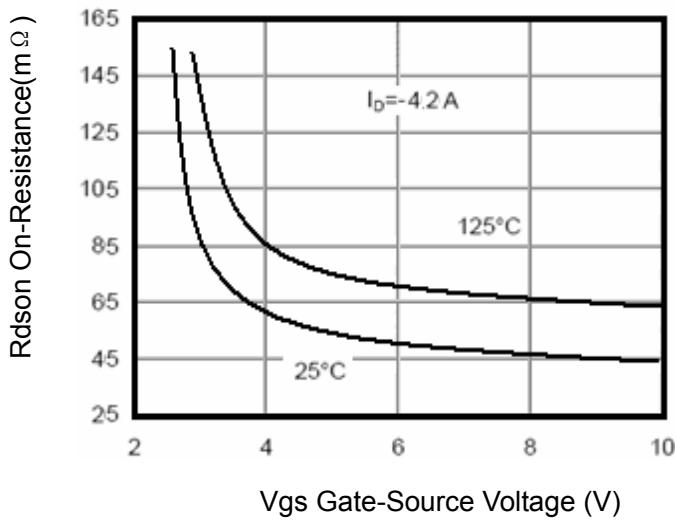
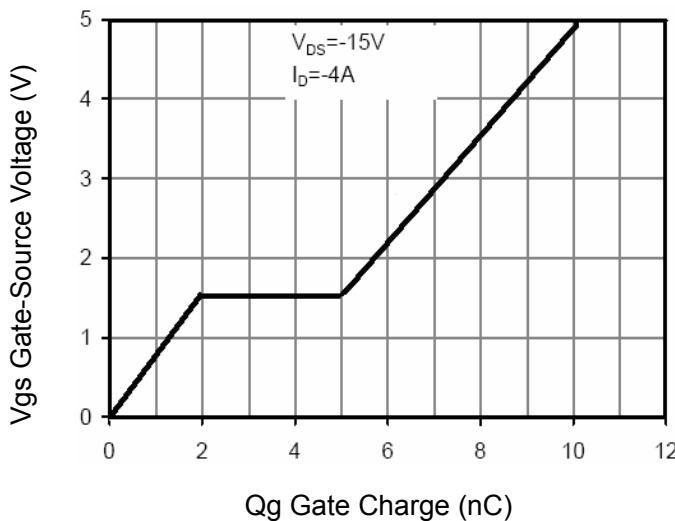
Figure 9 $R_{DS(on)}$ vs V_{GS} 

Figure 11 Gate Charge

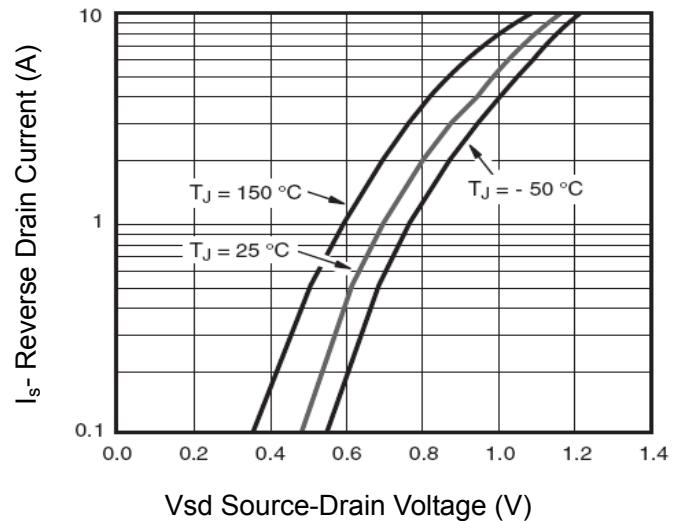


Figure 12 Source-Drain Diode Forward

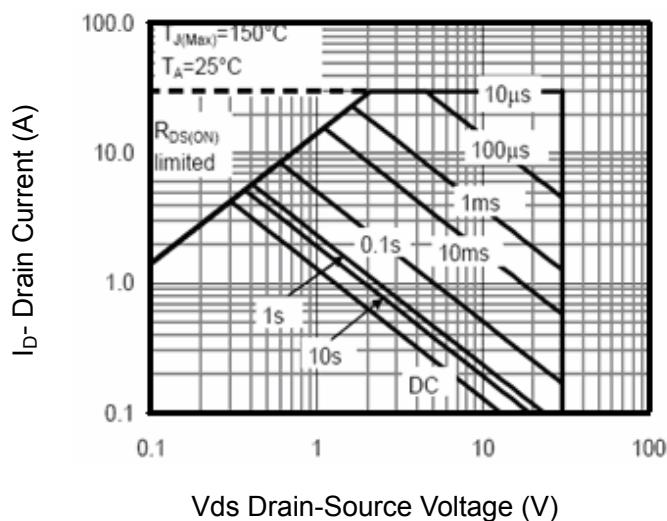


Figure 13 Safe Operation Area

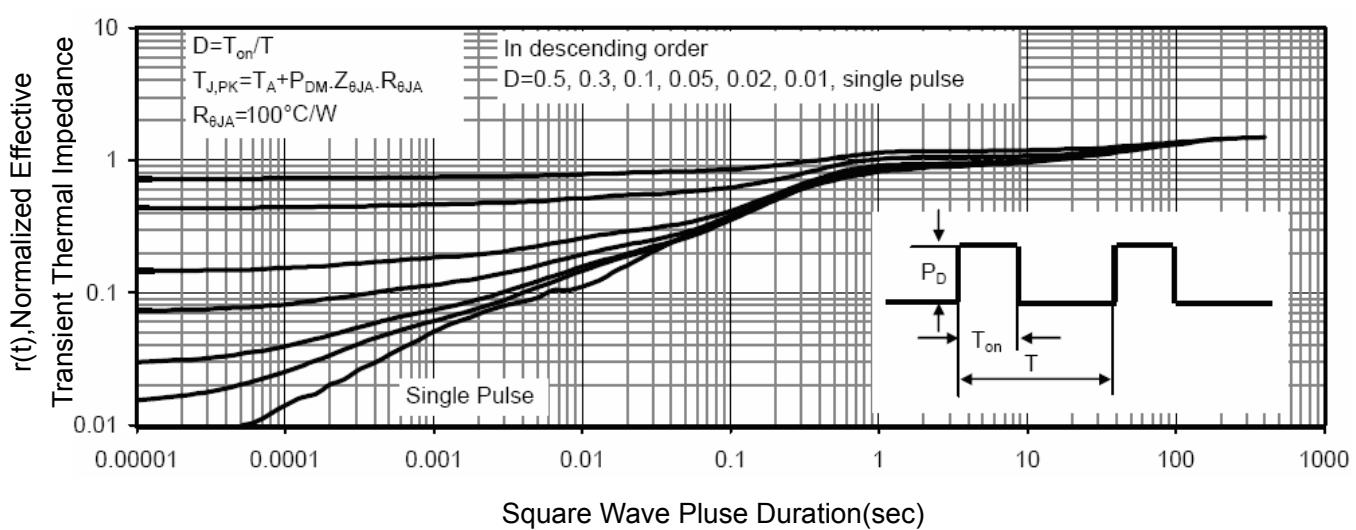
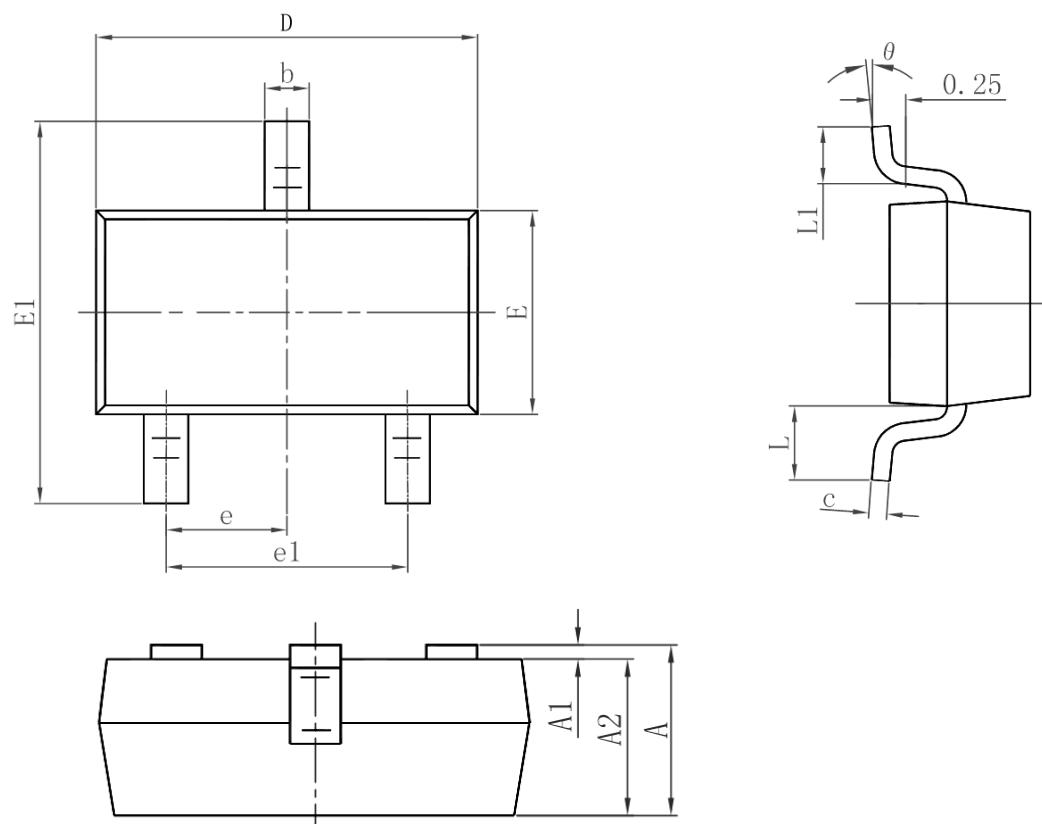


Figure 14 Normalized Maximum Transient Thermal Impedance

Package Information : SOT-23



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|---------------|----------------------------------|-------------|-----------------------------|-------------|
| | Min. | Max. | Min. | Max. |
| A | 0.900 | 1.150 | 0.035 | 0.045 |
| A1 | 0.000 | 0.100 | 0.000 | 0.004 |
| A2 | 0.900 | 1.050 | 0.035 | 0.041 |
| b | 0.300 | 0.500 | 0.012 | 0.020 |
| c | 0.080 | 0.150 | 0.003 | 0.006 |
| D | 2.800 | 3.000 | 0.110 | 0.118 |
| E | 1.200 | 1.400 | 0.047 | 0.055 |
| E1 | 2.250 | 2.550 | 0.089 | 0.100 |
| e | 0.950 TYP. | | 0.037 TYP. | |
| e1 | 1.800 | 2.000 | 0.071 | 0.079 |
| L | 0.550 REF. | | 0.022 REF. | |
| L1 | 0.300 | 0.500 | 0.012 | 0.020 |
| θ | 0° | 8° | 0° | 8° |